NOTES:
1. THIS DOCUMENT DESCRIBES A COMMERCIAL RELIABILITY FILTER AND FEEDTHRU DISC OIDAL CAPACITOR MANUFACTURED, SCREENED AND TESTED PER MIL-PRF-28861 EXCEPT WHERE NOTED.
2. DESIGNED TO BE LASER WELDED TO AN ALUMINUM HOUSING.
3. HERMETIC LEAK RATE: LESS THAN OR EQUAL TO 1X10^-9 CC/SEC He AT 1 ATM DIFFERENTIAL PRESSURE.

4. ELECTRICAL REQUIREMENTS:
   A. DIELECTRIC WITHSTANDING VOLTAGE: THERE SHALL BE NO EVIDENCE OF BREAKDOWN, FLASHOVER OR DETERIORATION WHEN SUBJECTED TO 250 VOLTS DC
   B. INSULATION RESISTANCE: SHALL BE > 100,000 MEGOHMS AT 100 VDC
   C. INSERTION LOSS: PER TABLE I
   D. CAPACITANCE TO GROUND: SHALL BE 6800pF ±5%
   E. DC RESISTANCE: SHALL BE 0.02 OHMS MAXIMUM
   F. VOLTAGE RATING: SHALL BE 100 VDC MAXIMUM
   G. CURRENT RATING: 20 AMPERES DC ; TEMPERATURE RISE 25°C WHEN TESTED PER MIL-PRF-28861

5. VISUAL, MECHANICAL, AND ELECTRICAL TESTING SHALL BE IN ACCORDANCE WITH MIL-PRF-28861 AND TABLE I. THE REQUIREMENTS PER TABLE I TAKE PRECEDENCE OVER MIL-PRF-28861.

6. MATERIALS:
   A. SHELL: EXPLOSION BONDED STAINLESS STEEL TO 4XXX-SERIES ALUMINUM
   B. CONTACTS: CHROMIUM COPPER ALLOY 182 IAW MIL-C-19311
   C. INSULATORS: KRYOFLEX 313 PROPRIETARY POLYCRYSTALLINE CERAMIC
   D. SOLDER: PIN TO CAP 96% Sn / 4% Ag, CAP TO CASE 80% Au / 20%
   E. EPOXY: RTV 615
   F. ENCAPSULANT: BACON P85

7. FINISH:
   PIN, FERRULE, AND SHELL: ELECTROLYTIC NICKEL PLATE IAW QQ-N-290, .000100/.000250 THICK.
   GOLD PLATE IAW ASTM B 488, TYPE I, CODE A, .000075/.000150 THICK.
   SHELL: UNPLATED IN AREA SHOWN TO ALLOW LASER WELD.

8. WIRE BOND FLAT SHALL BE .028 MIN WIDE BY .040 MIN LONG.

9. ORDERING INFORMATION:
   PLEASE SPECIFY ACCORDING TO THE FOLLOWING
   BASE PART NUMBER 94758 - C - I
   TESTING LEVEL: C-COMMERCIAL/MILITARY LEVEL

TABLE I

<table>
<thead>
<tr>
<th>PART NUMBER</th>
<th>INSULATION RESISTANCE AT +105°C</th>
<th>RATED DC CURRENT</th>
<th>MAX. DF OF 100</th>
<th>CAPACITANCE (pF)</th>
<th>MIN. INSERTION LOSS (dB)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>5 A</td>
<td>2.5</td>
<td>18</td>
<td>6,800</td>
<td>10 MHz</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>35</td>
<td></td>
<td>100 MHz</td>
</tr>
<tr>
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<td></td>
<td></td>
<td>57</td>
<td></td>
<td>1 GHz</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>65</td>
<td></td>
<td>10 GHz</td>
</tr>
</tbody>
</table>

NOTES:
1. Insertion loss measurements shall be made under full load over the frequency range of 1 MHz to 10 MHz. Insertion loss requirements above this frequency range shall be made under no load. For B circuits insertion loss shall be made under no load on.
2. The insertion loss requirements between any two adjacent specified frequencies shall be made that of the lower of the two frequencies in order to accommodate resonant dips.
3. The ten (10) GHz test need not be performed if guaranteed by the manufacturer.